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012304

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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10/763343

012304

Transmitted herewith for filing under 37 CFR 1.53(b) are the specification and claims of the nonprovisional patent application of:

Katsuhiko Aisu
Inventor(s)

SEMICONDUCTOR INTEGRATED CIRCUIT AND AMPLIFIER FOR SUPPRESSING POP SOUND WHILE MINIMIZING VOLTAGE TRANSITION SETTling TIME

Title of Invention

APPLICATION ELEMENTS ENCLOSED:

1. X Specification, total pages 12 including:
 - a. 2 pages of claims (6 claims)
 - b. 1 page(s) Abstract
2. X 8 sheets of informal X formal drawings (Figs. 1A-8B)
3. Oath or declaration of Applicant(s) (total pages)
 - a. Newly executed (original)
 - b. Unexecuted
 - c. Copy from a prior application (37 CFR 1.63(d))
(for continuation/divisional with Item 14 completed)
4. Deletion of Inventor(s)
Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b)
5. Incorporation By Reference
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied in item 3b is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
6. Microfiche Computer Program (Appendix)

APPLICATION PARTS ENCLOSED:

7. An assignment (cover sheet & document(s))
8. 37 CFR 3.73(b) Statement (when there is an assignee)
9. Power of Attorney
 - a. Newly executed (original)
 - b. Unexecuted
 - c. Copy from a prior application

- | | |
|--------------------------|------------------------------------------------------------------------|
| <input type="checkbox"/> | Continuation of prior application No.: _____ filed _____ |
| <input type="checkbox"/> | Divisional of prior application No.: _____ filed _____ |
| <input type="checkbox"/> | Continuation-in-part (CIP) of prior application No.: _____ filed _____ |

- The filing fee is calculated as follows:

					Rate			Fee	
	Number Filed		Number Extra*		Small Entity	Other Entity		Small Entity	Other Entity
Total Claims	6 - 20	=	0	X	\$9	\$18	=	\$0	\$0
Indep. Claims	2 - 3	=	0	X	\$43	\$86	=	\$0	\$0
Multiple Dependent Claims Presented: Yes_____ No <u> X </u>					\$145	\$290	=	\$0	\$0
If the difference in column 1 is less than zero, enter "0" in column 2					Basic Fee			\$385.00	\$770.00
					Total Fee			\$	\$770.00


18. X A check in the amount of \$770.00 to cover the filing fee.
19. A check in the amount of \$ for recordation of the Assignment.
20. X The Commissioner is hereby authorized to charge any additional fees which may be required in connection with the following or credit any overpayment to Deposit Account No. 03-3125.
- X Filing fees under 37 C.F.R. §1.16.
- X Patent application processing fees under 37 C.F.R. §1.17.
- The issue fee set in 37 C.F.R. §1.18 at or before mailing of the Notice of Allowance, pursuant to 37 C.F.R. §1.311(b).
21. X Three copies of this sheet are enclosed.
22. Other (identify) _____

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Respectfully submitted,

January 23, 2004
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Katsuhiko Aisu
Serial No. : Not Yet Known
Date Filed : Herewith
For : SEMICONDUCTOR INTEGRATED CIRCUIT AND AMPLIFIER
FOR SUPPRESSING POP SOUND WHILE MINIMIZING VOLTAGE
TRANSITION SETTLING TIME

Mail Stop Patent Application

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


Sir:

EXPRESS MAIL LETTER OF TRANSMITTAL

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Date of Deposit: January 23, 2004

I hereby certify that the above-identified application of Katsuhiko Aisu, filed herewith, including Specification (12 pages), Claims (1-6, 2 pages), Abstract (1 page), 8 sheets of formal drawings (Figs. 1A-8B), Information Disclosure Statement (including Form PTO-1449 and copies of references), certified copy of Japanese Patent Application No. 2003-017506, application transmittal in triplicate, a check in the amount of \$770.00 for filing fee and a return stamped postcard, is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated above and is addressed to the Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450.


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